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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/763,064	04/03/2001	Kiyotaka Sasanouchi	43890-482	2705
20277 7.	590 04/25/2002			
MCDERMOTT WILL & EMERY			EXAMINER	
600 13TH STR WASHINGTO	EET, N.W. N, DC 20005-3096		KERVEROS, JAMES C	
		·	ART UNIT	PAPER NUMBER
			2858	7
			DATE MAILED: 04/25/2002	/

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)		
		09/763,064	SASANOUCHI ET AL.		
Office Action Summary		Examiner	Art Unit		
		James C Kerveros	2858		
Period fo	The MAILING DATE of this communication ap				
A SH THE - Exte after - If the - If NC - Failu - Any	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a repl period for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by statute eply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tily within the statutory minimum of thirty (30) dawill apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONI g date of this communication, even if timely file	imely filed ys will be considered timely. in the mailing date of this communication.		
1) 🖂	Responsive to communication(s) filed on 03 /	<u> April 2001</u> .			
2a) <u></u> □	This action is FINAL . 2b)⊠ Th	is action is non-final.			
3)□ Dispositi	Since this application is in condition for allows closed in accordance with the practice under on of Claims	ance except for formal matters, p Ex parte Quayle, 1935 C.D. 11, 4	rosecution as to the merits is 453 O.G. 213.		
4)⊠	Claim(s) $\underline{1-5}$ is/are pending in the application.		·		
•	4a) Of the above claim(s) is/are withdra	vn from consideration.			
5)	Claim(s) is/are allowed.				
6)⊠	Claim(s) <u>1-5</u> is/are rejected.				
7)🖂	Claim(s) <u>5</u> is/are objected to.				
8)□	Claim(s) are subject to restriction and/or	r election requirement.	•		
Application	on Papers	·			
9)⊠ 7	he specification is objected to by the Examiner				
10)⊠ T	he drawing(s) filed on <u>03 April 2001</u> is/are: a)[\square accepted or b) $igties$ objected to by t	he Examiner.		
	Applicant may not request that any objection to the	drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).		
11)□ T	he proposed drawing correction filed on	is: a) ☐ approved b) ☐ disappro	ved by the Examiner.		
_	If approved, corrected drawings are required in rep				
12)∐ T	he oath or declaration is objected to by the Exa	aminer.			
Priority u	nder 35 U.S.C. §§ 119 and 120				
13)🛛 🛚	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a))-(d) or (f).		
a)[∑	〗All b) ☐ Some * c) ☐ None of:				
•	1. Certified copies of the priority documents	have been received.			
2	2. Certified copies of the priority documents have been received in Application No				
	B. Copies of the certified copies of the priori application from the International Bure the attached detailed Office action for a list of	ty documents have been receive eau (PCT Rule 17.2(a)).	d in this National Stage		
	knowledgment is made of a claim for domestic				
_ a)	☐ The translation of the foreign language prov cknowledgment is made of a claim for domestic	risional application has been rece	eived.		
Attachment(••			
2) Notice 3) Informa	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(s) <u>5</u> .		(PTO-413) Paper No(s) atent Application (PTO-152)		
O-326 (Rev.	0.4.0.43	on Summary	Part of Paner No. 7		

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DETAILED ACTION

Drawings

Figure 14 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Specification

The abstract of the disclosure is objected to because of minor informalities.

On line 1 in the opening sentence of the abstract, please delete "It is an object of the invention to present".

Correction is required. See MPEP § 608.01(b).

Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

Claim Objections

Claim 5 is objected to because of the following informalities:

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In Claim 5, on line 3, please replace "an" with ----a----.

Appropriate correction is required.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Szczyrbak et al. (US 5479096) in view of Applicant's admitted prior art.

Regarding Claim 1, Szczyrbak discloses a sensing pressure system with digital temperature and measurement gain and offset correction, comprising:

Pressure sensitive resistance transducer (12) connected to power supply (10), where the transducer is a physical quantity such as pressure, acceleration or torque.

The combination of power supply and transducer represent a measurement parameter transducer (14), as shown in FIGURE 1.

A controller block (38 and 40) for detecting an analog electrical output indicative of the measurement parameter provided resistance transducer (12) and supplying the analog electrical output to preamp (16), which amplifies the transducer analog signal and where amplifier (28) amplifies the temperature corrected analog output by providing the desired analog sensor output. The controller further, comprising:

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An A/D analog-to-digital converter (30) receives a signal from the pressure sensitive resistance transducer (12) through the analog preamplifier (16) output, (FIGURE 1).

A memory transducer-store (32), which is a Programmable Read Only Memory (PROM), stores the measurement parameter gain and offset corrections for different values of the analog preamplifier output (16), which correct for the transducers gain and offset, respectively. The digital output of the transducer store 32 is supplied to transducer gain block 34 and offset block 36.

A D/A converter compensating a signal from the A/D (30) from the pressure sensitive resistance transducer (12) electrical characteristics due to resistance changes of the transducer, where the compensation digital output value of the transducer is stored in memory (32) and is supplied to the transducer gain block 34 and offset block 36. The blocks can be electronic potentiometers or digital-to-analog converters (D/A).

The adjustment values from gain block 34 and offset block 36 provide inputs to an analog signal for correction of the gain and offset of the amplifier 28.

Regarding Claim 1, Szczyrbak in (US 5479096) does not disclose the detailed structural features of the pressure sensitive resistance element. However, Applicant's own admitted prior art, FIGURE 14, shows a conventional piezoresistive pressure sensitive resistance sensor comprising two insulating substrates (51,52) upper and lower disposed face to face and a pressure sensitive conductor (54) interposed the two insulating substrates (51, 52). It would have been obvious at the time the invention was made to a person of ordinary skill in the art to employ a conventional piezoresistive

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pressure resistance sensor as shown by the Applicant's own admitted prior art (FIGURE 14) in the device of Szczyrbak for the purpose of detecting electrical characteristics indicative of a measured parameter provided by conventional piezoresistive pressure resistance transducer sensor.

Regarding Claim 2, Szczyrbak discloses a controller block (38 and 40) further comprising a temperature sensor (18), which provides an electrical output related to the temperature of the transducer 12. The temperature store (22) provides a temperature gain and a temperature offset correction outputs. The gain correction output is applied to the gain block 24 and the offset correction is applied to offset block 26.

Regarding Claim 4, Szczyrbak discloses a reference output voltage source gain block 34 and offset block 36 for providing an offset to amplifier 28 output.

An error amplifier (28), for receiving the reference output voltage and which amplifies the transducer analog signal and the temperature corrected analog output by providing the desired analog sensor output.

Regarding Claims 3 and 5, Szczyrbak fails to disclose a plurality of output terminals corresponding to plurality of resistance elements and detection output showing the abnormality of the signal processing. In reference to Claim 3, Szczyrbak discloses a transducer (12) comprising a plurality of resistance elements in a bridge connection with a constant voltage supply 50 across junctions A and C, shown in FIGURE 2. The A/D converter 30 converts the signal from the transducer 12 of the bridge type connection and stores the information in memory 58. It would have been

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obvious to a person of ordinary skill in the art to use the plurality resistance elements of the bridge connection in the device of Szczyrbak by adding plurality of output terminals

with abnormality detection output for the purpose of sensing a plurality of resistance

elements and for generating outputs indicative of the measurement parameter provided

by the plurality resistance elements.

The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

Raynes (US 5146788) ISSUED: September 15, 1992, apparatus and method for

a temperature compensation of a catheter tip pressure transducer.

Any inquiry concerning this communication from the examiner should be directed

to JAMES C. KERVEROS at (703) 305-1081 or the examiner's supervisor, N. LE at

(703) 308-0750.

The official Fax numbers for the organization are (703-872-9318) Before-Final

and (703-872-9319) After-Final Office actions. Any inquiry of a general nature relating to

this application should be directed to the receptionist at (703) 305-4900.

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James C. Kerveros

Patent Examiner.

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US Patent/and Trademark Office

Date: April 18, 2002